

Lesson Plan

Subject- Computer Architecture

Lesson Plan- 17 Weeks (July – Nov 2019)

Week	Date	BCA II BCA- 233
1	16- July-2019	-
	17- July-2019	-
	18- July-2019	Basic Computer Organization and Design:
	19- July-2019	Instruction Codes, Computer registers
	20- July-2019	Design of accumulator logic
	21- July-2019	Sunday
2	22- July-2019	-
	23- July-2019	-
	24- July-2019	-
	25- July-2019	Computer Instructions
	26- July-2019	Timing and Control
	27- July-2019	Instruction Cycle
	28- July-2019	Sunday
3	29- July-2019	-
	30- July-2019	-
	31- July-2019	Holiday
	1- Aug- 2019	RISC, CISC.
	2- Aug- 2019	Input-Output and Interrupt
	3- Aug- 2019	Memory reference instructions
	4- Aug- 2019	Sunday
4	5- Aug- 2019	-
	6- Aug- 2019	-
	7- Aug- 2019	-
	8- Aug- 2019	RISC, CISC.
	9- Aug- 2019	Central Processing Unit:
	10- Aug- 2019	General registers Organization
	11- Aug- 2019	Sunday
5	12- Aug- 2019	Holiday
	13- Aug- 2019	-
	14- Aug- 2019	-
	15- Aug- 2019	Holiday
	16- Aug- 2019	Stack Organization
	17- Aug- 2019	Instruction formats
	18- Aug- 2019	Sunday
6	19- Aug- 2019	-
	20- Aug- 2019	-
	21- Aug- 2019	-
	22- Aug- 2019	Addressing Modes
	23- Aug- 2019	Program Interrupt
	24- Aug- 2019	Memory Organization

	25- Aug- 2019	Sunday
7	26- Aug- 2019	-
	27- Aug- 2019	-
	28- Aug- 2019	-
	29- Aug- 2019	Memory hierarchy
	30- Aug- 2019	Auxiliary Memory
	31- Aug- 2019	Associative Memory
	1- Sept- 2019	Sunday
8	2- Sept- 2019	-
	3- Sept- 2019	-
	4- Sept- 2019	-
	5- Sept- 2019	Virtual Memory
	6- Sept- 2019	Data Transfer and Manipulation, Program Control
	7- Sept- 2019	Cache memory
	8- Sept- 2019	Sunday
9	9- Sept- 2019	-
	10- Sept- 2019	-
	11- Sept- 2019	-
	12- Sept- 2019	Memory Management Hardware
	13- Sept- 2019	Input Output Organization
	14- Sept- 2019	Peripheral devices
	15- Sept- 2019	Sunday
10	16- Sept- 2019	-
	17- Sept- 2019	-
	18- Sept- 2019	-
	19- Sept- 2019	Input-Output Interface
	20- Sept- 2019	Asynchronous data transfer
	21- Sept- 2019	Revision
	22- Sept- 2019	Sunday
11	23- Sept- 2019	Holiday
	24- Sept- 2019	-
	25- Sept - 2019	-
	26- Sept - 2019	Modes of Transfer
	27- Sept - 2019	Priority Interrupt
	28- Sept - 2019	Direct Memory Access(DMA)
	29- Sept - 2019	Sunday
12	30- Sept - 2019	-
	1- Oct- 2019	-
	2- Oct- 2019	Holiday
	3- Oct- 2019	Input-Output Processor(IOP)
	4- Oct- 2019	Design of Basic computer
	5- Oct- 2019	Register Transfer and Microoperations
	6- Oct- 2019	Sunday
13	7- Oct- 2019	-
	8- Oct- 2019	Holiday
	9- Oct- 2019	-
	10- Oct- 2019	Holiday
	11- Oct- 2019	Register Transfer Language (RTL),register transfer, Bus and Memory Transfers

	12- Oct- 2019	Logic Microoperations
	13- Oct- 2019	Sunday
14	14- Oct- 2019	-
	15- Oct- 2019	-
	16- Oct- 2019	-
	17- Oct- 2019	Holiday
	18- Oct- 2019	Arithmetic Microoperations
	19- Oct- 2019	Shift Microoperations
	20- Oct- 2019	Sunday
	15	21- Oct- 2019
22- Oct- 2019		-
23- Oct- 2019		-
24- Oct- 2019		Arithmetic Logic Shift Unit
25- Oct- 2019		Diwali Break
26- Oct- 2019		
27- Oct- 2019		
28- Oct- 2019		
16	29- Oct- 2019	
	30- Oct- 2019	MicroprogrammedControl:Control memory
	31- Oct- 2019	address sequencing, microprogram sequencer
	1- Nov- 2019	Holiday
	2- Nov- 2019	Holiday
	3- Nov- 2019	Sunday
	4- Nov- 2019	-
17	5- Nov- 2019	-
	6- Nov- 2019	-
	7- Nov- 2019	Design of Control Unit, Question paper discussion
	8- Nov- 2019	Discussion over Previous Year papers
	9- Nov- 2019	Revision
	10- Nov- 2019	Sunday