Lesson Plan

Subject- Computer Science and Applications

Lesson Plan- 16 Weeks (Nov 20 – Feb 21)

Week	Date	BCA(3rdSem) Computer System Architecture
1	2- Nov – 2020	-
	3- Nov – 2020	-
	4- Nov – 2020	-
	5- Nov – 2020	Basic Computer Organisation and Design:
	6- Nov – 2020	Instruction Codes, Computer registers
	7- Nov - 2020	Design of accumulator logic
2	8- Nov - 2020	Sunday
	9- Nov - 2020	-
	10- Nov - 2020	-
	11- Nov - 2020	-
	12- Nov - 2020	Computer Instructions
	13- Nov - 2020	Timing and Control
	14- Nov - 2020	Holiday
3	15- Nov - 2020	Sunday
	16- Nov - 2020	Holiday
	17- Nov - 2020	-
	18- Nov - 2020	-
	19- Nov - 2020	Instruction Cycle
	20- Nov - 2020	RISC, CISC.
	21- Nov - 2020	Input-Output and Interrupt
4	22- Nov - 2020	Sunday
	23- Nov - 2020	**Assignment 1**
	24- Nov - 2020	Holiday
	25- Nov - 2020	-
	26- Nov - 2020	Memory reference instructions
	27- Nov - 2020	RISC, CISC.
	28- Nov - 2020	Central Processing Unit:
5	29- Nov - 2020	Sunday
	30- Nov - 2020	Holiday
	1- Dec - 2020	-
	2- Dec - 2020	-
	3- Dec - 2020	General registers Organization
	4- Dec - 2020	**Sessional**
	5- Dec - 2020	Stack Organization
6	6- Dec - 2020	Sunday
	7- Dec - 2020	-
	8- Dec - 2020	-
	9- Dec - 2020	-
	10- Dec - 2020	Instruction formats
	11- Dec - 2020	Addressing Modes
	12- Dec - 2020	Program Interrupt

7	13- Dec - 2020	Sunday
	14- Dec - 2020	-
	15- Dec - 2020	-
	16- Dec - 2020	-
	17- Dec - 2020	Memory Organization
	18- Dec - 2020	Memory hierarchy
	19- Dec - 2020	Auxiliary Memory
8	20- Dec - 2020	Sunday
	21- Dec - 2020	-
	22- Dec - 2020	-
	23- Dec - 2020	-
	24- Dec - 2020	Associative Memory
	25- Dec - 2020	Virtual Memory
	26- Dec - 2020	Data Transfer and Manipulation, Program Control
	27- Dec - 2020	Sunday
9	28- Dec - 2020	-
	29- Dec - 2020	-
	30- Dec - 2020	-
	31- Dec - 2020	Cache memory
	1- Jan - 2021	Memory Management Hardware
	2- Jan - 2021	Input Output Organization
10	3- Jan - 2021	Sunday
	4- Jan - 2021	-
	5- Jan - 2021	-
	6- Jan - 2021	-
	7- Jan - 2021	Peripheral devices
	8- Jan - 2021	Input-Output Interface
	9- Jan - 2021	Class test
11	10- Jan - 2021	Sunday
	11- Jan - 2021	Holiday
	12- Jan - 2021	-
	13- Jan - 2021	-
	14- Jan - 2021	Asynchronous data transfer
	15- Jan - 2021	Revision
	16- Jan - 2021	Revision
12	17- Jan - 2021	Sunday
	18- Jan - 2021	-
	19- Jan - 2021	-
	20- Jan - 2021	Holiday
	21- Jan - 2021	Modes of Transfer
	22- Jan - 2021	Priority Interrupt
	23- Jan - 2021	Direct Memory Access(DMA)
13	24- Jan - 2021	Sunday
	25- Jan - 2021	-
	26- Jan - 2021	Holiday
	27- Jan - 2021	-
	28- Jan - 2021	Input-Output Processor(IOP)
	29- Jan - 2021	Design of Basic computer

	30- Jan - 2021	Register Transfer and Microoperations
14	31- Jan - 2021	Sunday
	1- Feb - 2021	-
	2- Feb - 2021	-
	3- Feb - 2021	-
	4- Feb - 2021	Register Transfer Language (RTL),register transfer, Bus and Memory Transfers
	5- Feb - 2021	Logic Microoperations
	6- Feb - 2021	Arithmetic Microoperations, Shift Microoperations
15	7- Feb - 2021	Sunday
	8- Feb - 2021	-
	9- Feb - 2021	-
	10- Feb - 2021	-
	11- Feb - 2021	Arithmetic Logic Shift Unit, MicroprogrammedControl:Control memory
	12- Feb - 2021	address sequencing, microprogram sequencer
	13- Feb - 2021	Design of Control Unit, Question paper discussion
	14- Feb - 2021	Sunday
16	15- Feb - 2021	-
	16- Feb - 2021	-
	17- Feb - 2021	-
	18- Feb - 2021	Revision
	19- Feb - 2021	Question paper discussion
	20- Feb - 2021	Revision