SANATAN DHARMA COLLEGE, AMBALA CANTT
College with Potential for Excellence, UGC,New Delhi
NAAC Accredited Grade "A+" with CGPA 3.51 in 3rd cycle
ISO 9001:2015 & ISO 14001:2015 Certified

Department of Computer Science
Lesson Plan (Session 2021-2022)

Class: BCA　　　　　　　　**Sem: I**　　　　　　**Sec-A & B Course Code: BCA-114**
Nomenclature: Logical Organization of Computers-I
Duration: 16 Weeks　　　　**Dates:- Oct-Jan, 2022**

SYLLABUS

BCA-114 Logical Organization of Computers-I

Maximum Marks: 100　　　　**External: 80**　　　　**Minimum Pass Marks: 35**
Internal: 20　　　　　　　　**Time: 3 hours**

Note: Examiner will be required to set Nine Questions in all. First Question will be compulsory, consisting of objective type/short-answer type questions covering the entire syllabus. In addition to that eight more questions will be set, two questions from each Unit.Student will be required to attempt FIVE questions in all. Question Number 1 will be compulsory. In addition to compulsory question, student will have to attempt four more questions selecting one question from each Unit.

UNIT - I

Information Representation: Number Systems, Binary Arithmetic, Fixed-point and Floating-point representation of numbers, BCD Codes, Error detecting and correcting codes, Character Representation – ASCII, EBCDIC.

UNIT - II

Binary Logic: Boolean Algebra, Boolean Theorems, Boolean Functions and Truth Tables, Canonical and Standard forms of Boolean functions, Simplification of Boolean Functions – Venn Diagram, Karnaugh Maps.

UNIT - III

Digital Logic: Basic Gates – AND, OR, NOT, Universal Gates – NAND, NOR, Other Gates – XOR, XNOR etc. implementations of digital circuits, Combinational Logic – Characteristics, Design Procedures, analysis procedures.

UNIT - IV

Combinational Circuits: Half-Adder, Full-Adder, Half-Subtractor, Full-Subtractor, Encoders, Decoders, Multiplexers, Demultiplexers, Comparators, Code Converters.

TEXT BOOKS
1. M. Morris Mano, Digital Logic and Computer Design, Prentice Hall of India Pvt. Ltd.
2. V. Rajaraman, T. Radhakrishnan, An Introduction to Digital Computer Design, Prentice Hall of India Pvt. Ltd.

Course Outcomes

After the completion of this course, prospective Computer professionals will have the ability to

CO-1	Identify, understand and apply Number Systems, Binary Arithmetic, Fixed-pointand Floatingpoint representation of numbers, BCD Codes, Error detecting and correcting codes, Character Representation – ASCII, EBCDIC.
CO-2	Apply Binary Logic: Boolean Algebra, Boolean Theorems, Boolean Functions andTruth Tables,Canonical and Standard forms of Boolean functions, Simplification of Boolean Functions – Venn Diagram, Karnaugh Maps.
CO-3	Design Digital Logic: Basic Gates – AND, OR, NOT, Universal Gates – NAND, NOR,OtherGates – XOR, XNOR etc. implementations of digital circuits, Combinational Logic – Characteristics, Design Procedures, analysis procedures
CO-4	Design Combinational Circuits: Half-Adder, Full-Adder, Half-Subtractor, Full-Subtractor,Encoders, Decoders, Multiplexers, Demultiplexers, Comparators, Code Converters.

S.No	Instructional Technique	Assessment Methods (AM)
1	Chalk & Talk	**Assignments**
2	ICT tools	**Quiz**
3	Group discussions	**Group Discussions**
4	Industrial visit	**Oral Tests**
5	Case studies	**Sessional**
6	Small Projects	**Presentations**
7	Workshop	**Seminar**
8	Spoken Tutorials	**University Exams**
9	Flipped Class	
10.	**E-Resources**	

Detailed Lesson Plan Sec-A

Week	Date	Topic to be Covered	Instructional Technique	Assessment Method
1	11.10.21	**Number Systems**	**2-(PPT/Projector)**	**----**
	12.10.21	**Types of Number Systems**	**1**	**1,2,3,4**
	13.10.21	**Binary Number system**	**1**	**1,2,3,4**
2	18.10.21	**Decimal Number system**	**1**	**1,2,3,4**
	19.10.21	Octal Number System	**1**	**1,2,3,4**
	20.10.21	**Hexadecimal Number System**	**1**	**1,2,3,4**
3	25.10.21	Binary Arithmetic	**1**	**1,2,3,4**
	26.10.21	Holiday		
	27.10.21	Fixed-point and Floating-point representation of numbers	2-(PPT/Projector)	1,2,3,4
4	1.11.21	**Fixed-point and Floating-point representation of numbers**	2-(PPT/Projector)	**1,2,3,4**
	2.11.21	**Error detecting and correcting codes**	2-(PPT/Projector)	1,2,3,4
	3.11.21	**Error detecting and correcting codes**	2-(PPT/Projector)	1,2,3,4
5	8.11.21	**Character Representation**	9,10	1,2,3,4
	9.11.21	ASCII, EBCDIC	9,10	1,2,3,4
	10.11.21	**Revision**	2-(PPT/Projector)	1,2,3,4
6	15.11.21	Holiday		
	16.11.21	Boolean Algebra	---	6
	17.11.21	**Boolean Theorems**	9	1,2,3,4,6
7	22.11.21	**Boolean Functions and Truth Tables**	6	1,2,3,4,
	23.11.21	**Boolean Functions and Truth Tables**	6	1,2,3,4,
	24.11.21	Diwali Vaccation		
8	29.11.21	**Canonical and Standard forms of Boolean functions**	6	1,2,3,4
	30.11.21	**Simplification of Boolean Functions**	6	1,2,3,4
	1.12.21	**Venn Diagram**		1,2,3,4
9	6.12.21	**Karnaugh Maps**	9,10	1,2,3,4
	7.12.21	**Karnaugh Maps**	9,10	
	8.12.21	**AND, OR, NOT,**	2-(PPT/Projector)	1,2,3,4
10	13.12.21	**Universal Gates – NAND, NOR**	2-(PPT/Projector)	1,2,3,4
	14.12.21	**Universal Gates – NAND, NOR**	2-(PPT/Projector)	1,2,3,4

	15.12.21	**Other Gates – XOR, XNOR**	2-(PPT/Projector)	1,2,3,4
11	20.12.21	**Characteristics of combinational circuits**	6	1,2,3,4
	21.12.21	**Design Procedures, analysis procedures.**	6	1,2,3,4
	22.12.21	**Sessional**	--	
12	27.12.21	**Combinational Circuits**	2-(PPT/Projector)	1,2,3,4
	28.12.21	**Half-Adder, Full-Adder**	2-(PPT/Projector)	1,2,3,4
	29.12.21	**Half-Subtractor,Full-Subtractor**	6	1,2,3,4
13	3.1.22	**Encoders**	2-(PPT/Projector)	1,2,3,4
	4.1.22	**Decoders**	6	1,2,3,4
	5.1.22	**Multiplexers**	2-(PPT/Projector)	1,2,3,4
14	10.1.22	Demultiplexers	9,10	1,2,3,4
	11.1.22	**Revision**		1,2,3,4
	12.1.22	**Class Test**	2-(PPT/Projector)	1,2,3,4
15	17.1.22	**Comparators**	2-(PPT/Projector)	1,2,3,4
	18.1.22	**Comparators**	6	1,2,3,4
	19.1.22	**Code Converters**	6	1,2,3,4
16	24.1.22	**Code Converters**	2-(PPT/Projector)	1,2,3,4
	25.1.22	Revision	6	1,2,3,4
	26.1.22	**Revision**	--	--

Detailed Lesson Plan Sec B

Week	Date	Topic to be Covered	Instructional Technique	Assessment Method
1	11.10.21	**Number Systems**	**1**	**----**
	12.10.21	**Types of Number Systems**	**1**	**1**
	13.10.21	**Binary Number system**	**1**	**1**
2	18.10.21	**Decimal Number system**	**1**	**1,2,3,4**
	19.10.21	Octal Number System	**1**	**1,2,3,4**
	20.10.21	**Hexadecimal Number System**	**1**	**1,2,3,4**
3	25.10.21	Binary Arithmetic	**2-(PPT/Projector)**	**1,2,3,4**
	26.10.21	**Fixed-point and Floating-point representation of numbers**	**2-(PPT/Projector)**	**1,2,3,4**
	27.10.21	**Fixed-point and Floating-point representation of numbers**	**2-(PPT/Projector)**	**1,2,3,4**
4	1.11.21	**Holiday**	**--**	**---**
	2.11.21	**Error detecting and correcting codes**	**2-(PPT/Projector)**	**1,2,3,4**
	3.11.21	**Error detecting and correcting codes**	**2-(PPT/Projector)**	**1,2,3,4**
5	8.11.21	**Character Representation**	**9,10**	**1,2,3,4**
	9.11.21	ASCII, EBCDIC	**9,10**	**1,2,3,4**
	10.11.21	**Revision**		**1,2,3,4**
6	15.11.21	Boolean Algebra	**---**	**6**
	16.11.21	**Boolean Theorems**	**9**	**1,2,3,4,6**
	17.11.21	**Boolean Functions and Truth Tables**	**6**	**1,2,3,4,**
7	22.11.21	**Canonical and Standard forms of Boolean functions**	**6**	**1,2,3,4,**
	23.11.21	**Simplification of Boolean Functions**	**6**	**1,2,3,4**
	24.11.21	**Venn Diagram**	**6**	**1,2,3,4**
8	29.11.21	**Diwali Vaccation**	**--**	**--**
	30.11.21	**Karnaugh Maps**	**2-(PPT/Projector)**	**1,2,3,4**
	1.12.21	**Holiday**		
	2-Nov-22	**Karnaugh Maps**	**2-(PPT/Projector)**	**1,2,3,4**
9	6.12.21	**Revision**	**2-(PPT/Projector)**	**1,2,3,4**
	7.12.21	**Holiday**		
	8.12.21	**AND, OR, NOT,**	**2-(PPT/Projector)**	**1,2,3,4**
10	13.12.21	**Universal Gates – NAND, NOR**	**2-(PPT/Projector)**	**1,2,3,4**
	14.12.21	**Universal Gates – NAND, NOR**	**6**	**1,2,3,4**

	15.12.21	**Other Gates – XOR, XNOR**	**6**	**1,2,3,4**
11	20.12.21	Characteristics of combinational circuits	**--**	**5**
	21.12.21	**Design Procedures, analysis procedures.**	**2-(PPT/Projector)**	**1,2,3,4**
	22.12.21	**Revision**	**2-(PPT/Projector)**	**1,2,3,4**
12	27.12.21	**Combinational Circuits**	**6**	**1,2,3,4**
	28.12.21	**Half-Adder, Full-Adder**	**2-(PPT/Projector)**	**1,2,3,4**
	29.12.21	**Half-Subtractor,Full-Subtractor**	**6**	**1,2,3,4**
13	3.1.22	**Encoders**	**2-(PPT/Projector)**	**1,2,3,4**
	4.1.22	Decoders	**9,10**	**1,2,3,4**
	5.1.22	**Multiplexers**	**9,10**	**1,2,3,4**
14	10.1.22	**Demultiplexers**	**2-(PPT/Projector)**	**1,2,3,4**
	11.1.22	**Revision**	**2-(PPT/Projector)**	**1,2,3,4**
	12.1.22	**Class Test**	**6**	**1,2,3,4**
15	17.1.22	**Comparators**	**6**	**1,2,3,4**
	18.1.22	**Comparators**	**2-(PPT/Projector)**	**1,2,3,4**
	19.1.22	**Code Converters**	**6**	**1,2,3,4**
16	24.1.22		**--**	**--**
	25.1.22		**--**	**--**
	26.1.22			